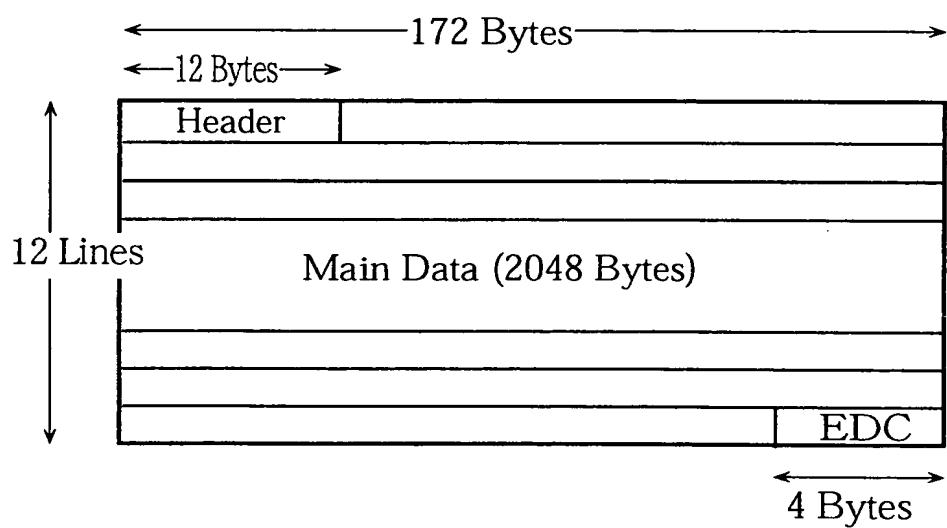


Fig. 1



10 34 41 42 43 45 46 47 48 49 4A 4B 4C 4D 4E 4F

Fig. 2

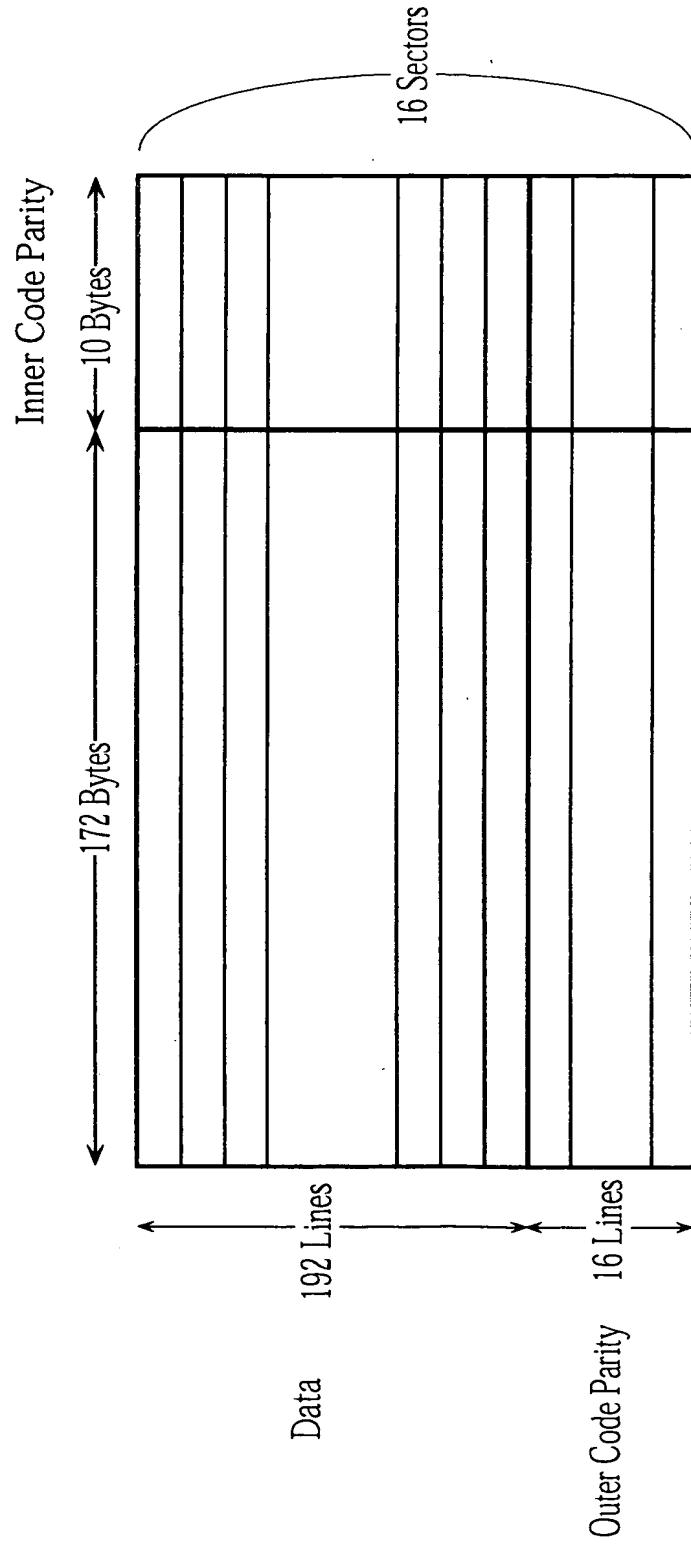


Fig. 3

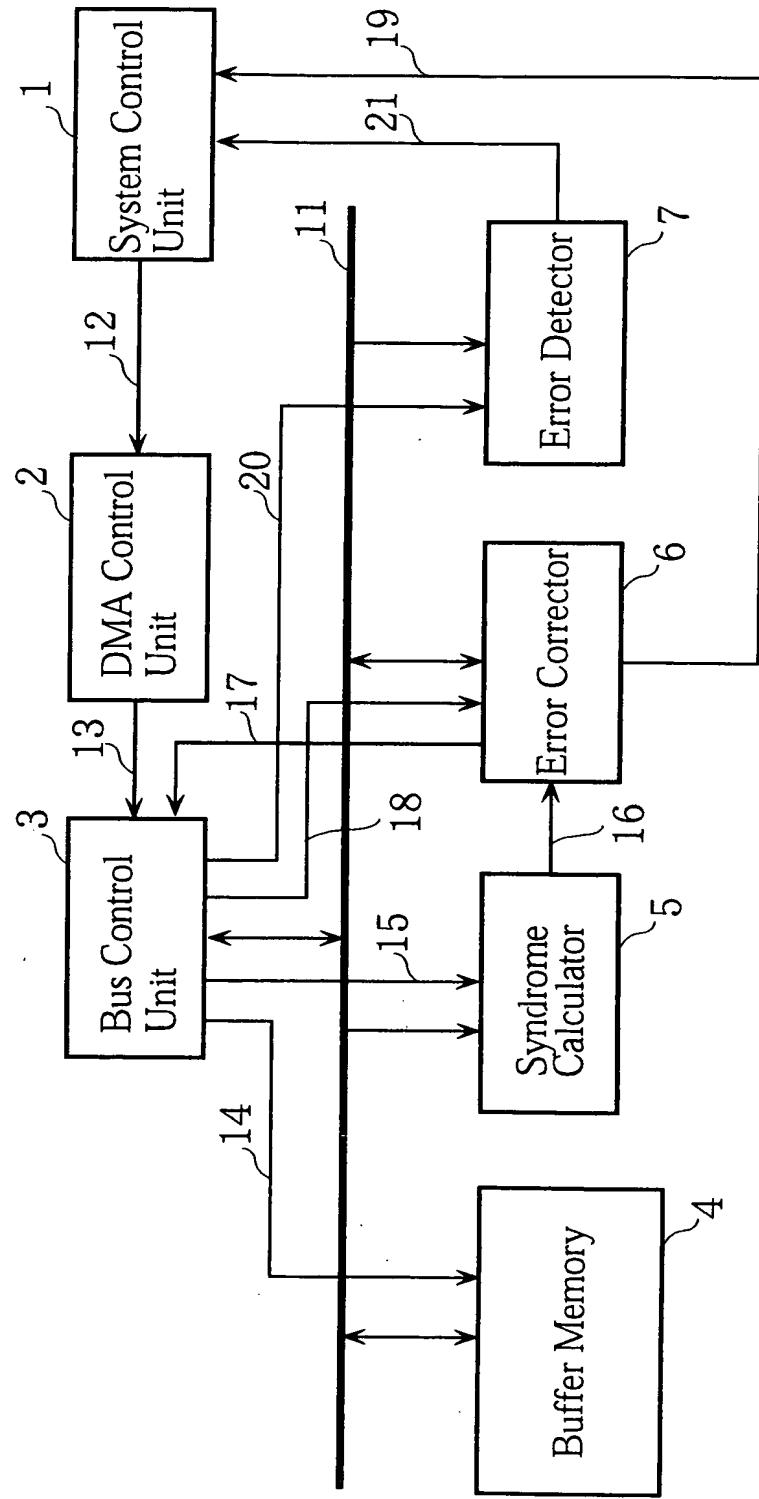


Fig. 4

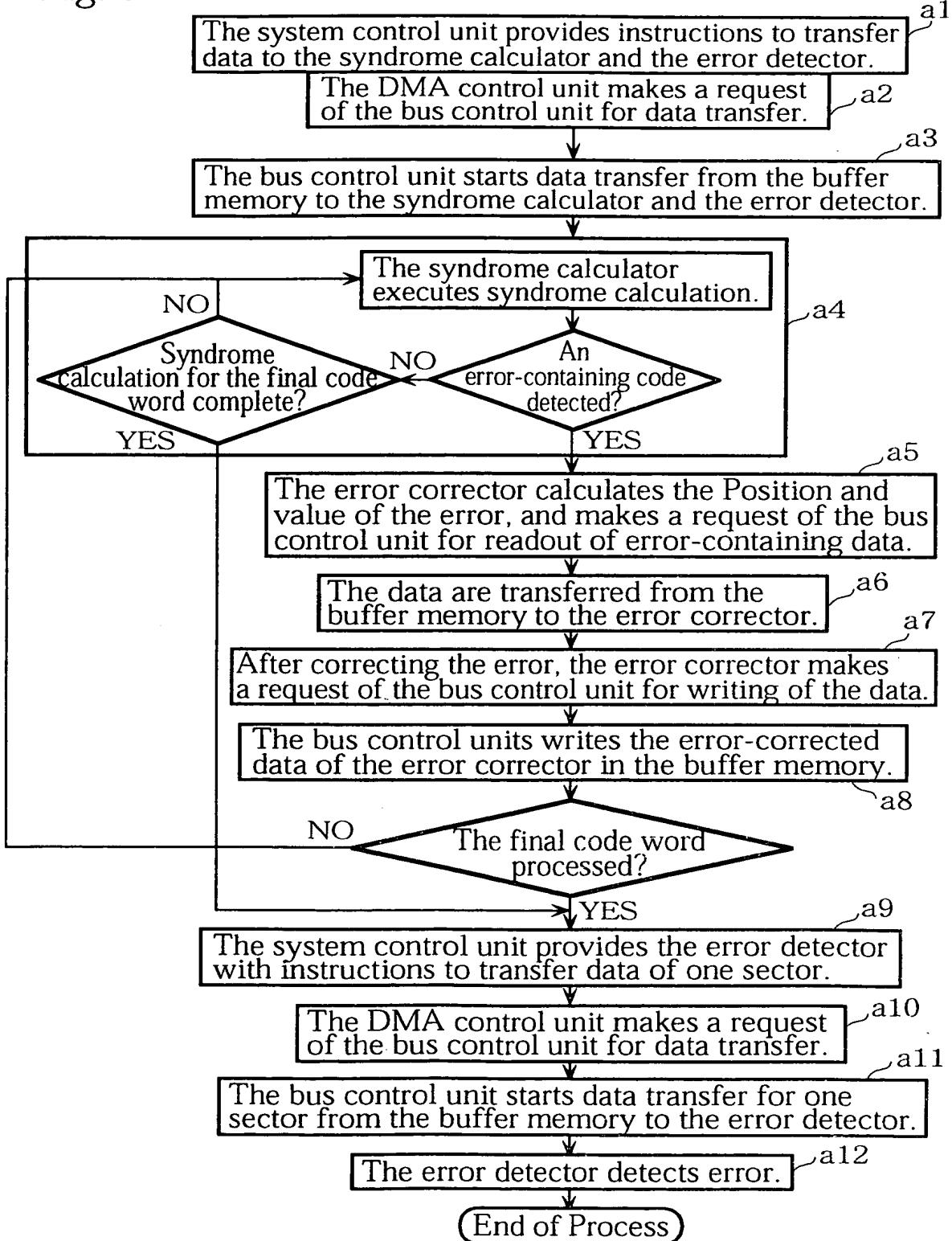


Fig. 5

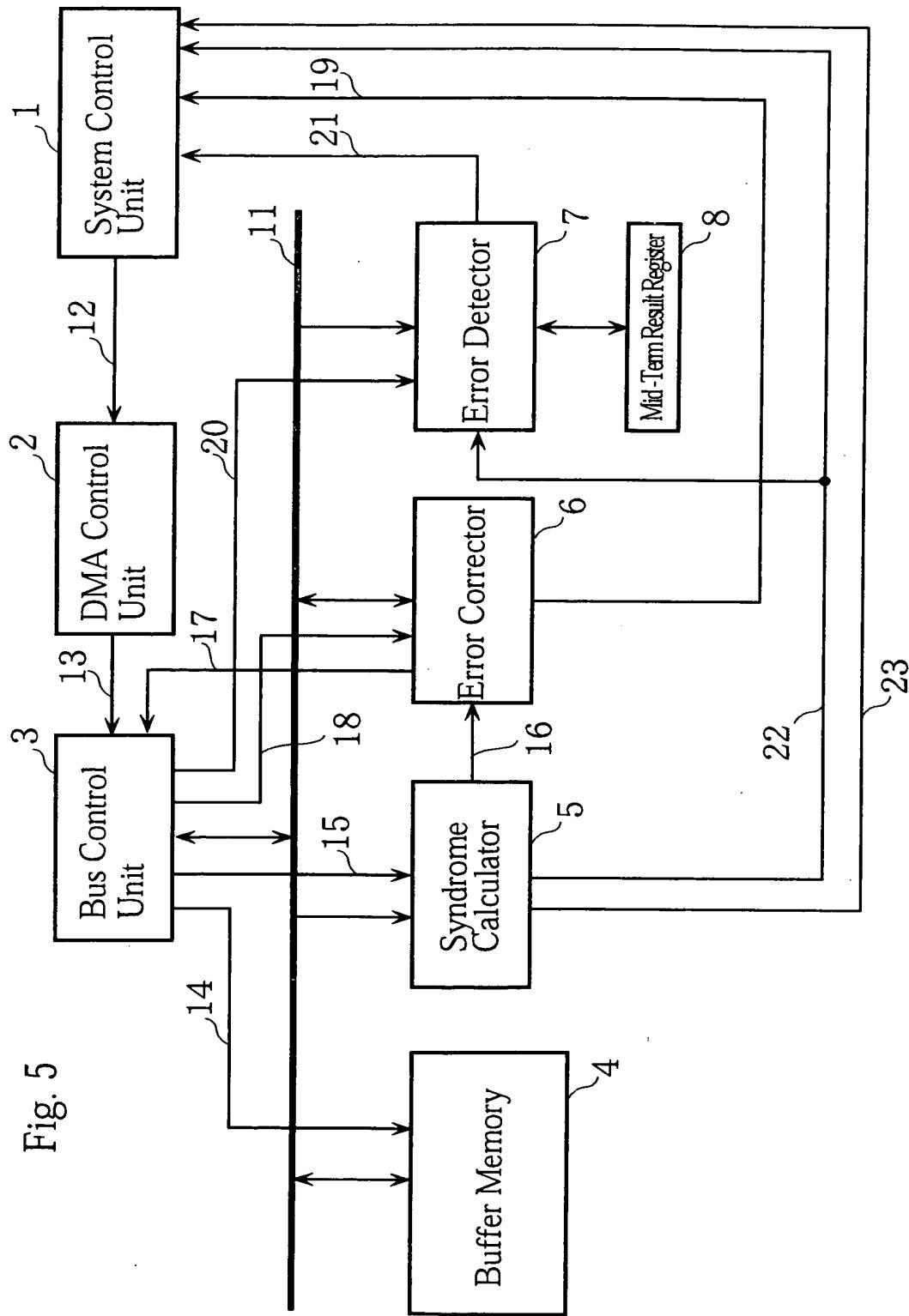


Fig. 6

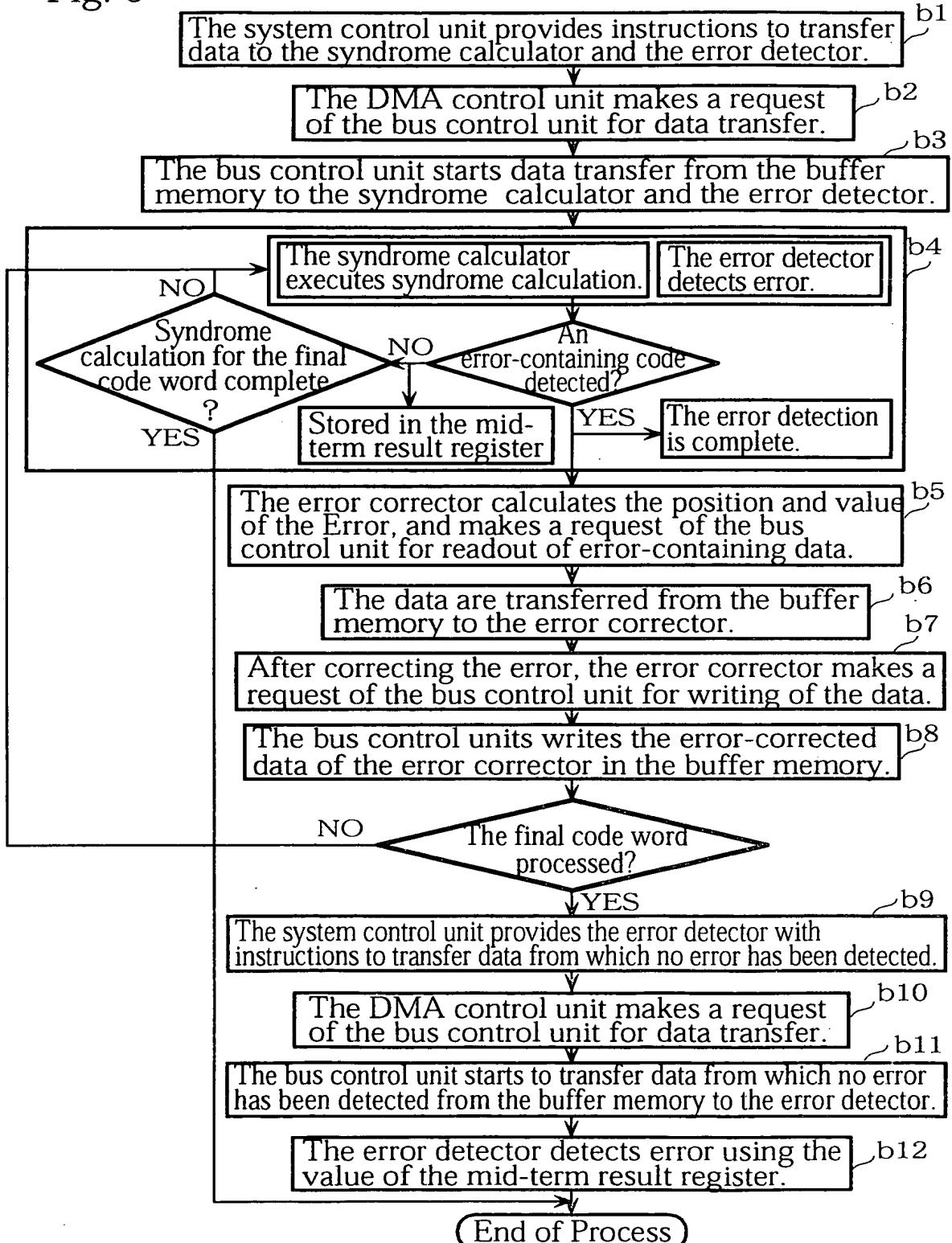


Fig. 7

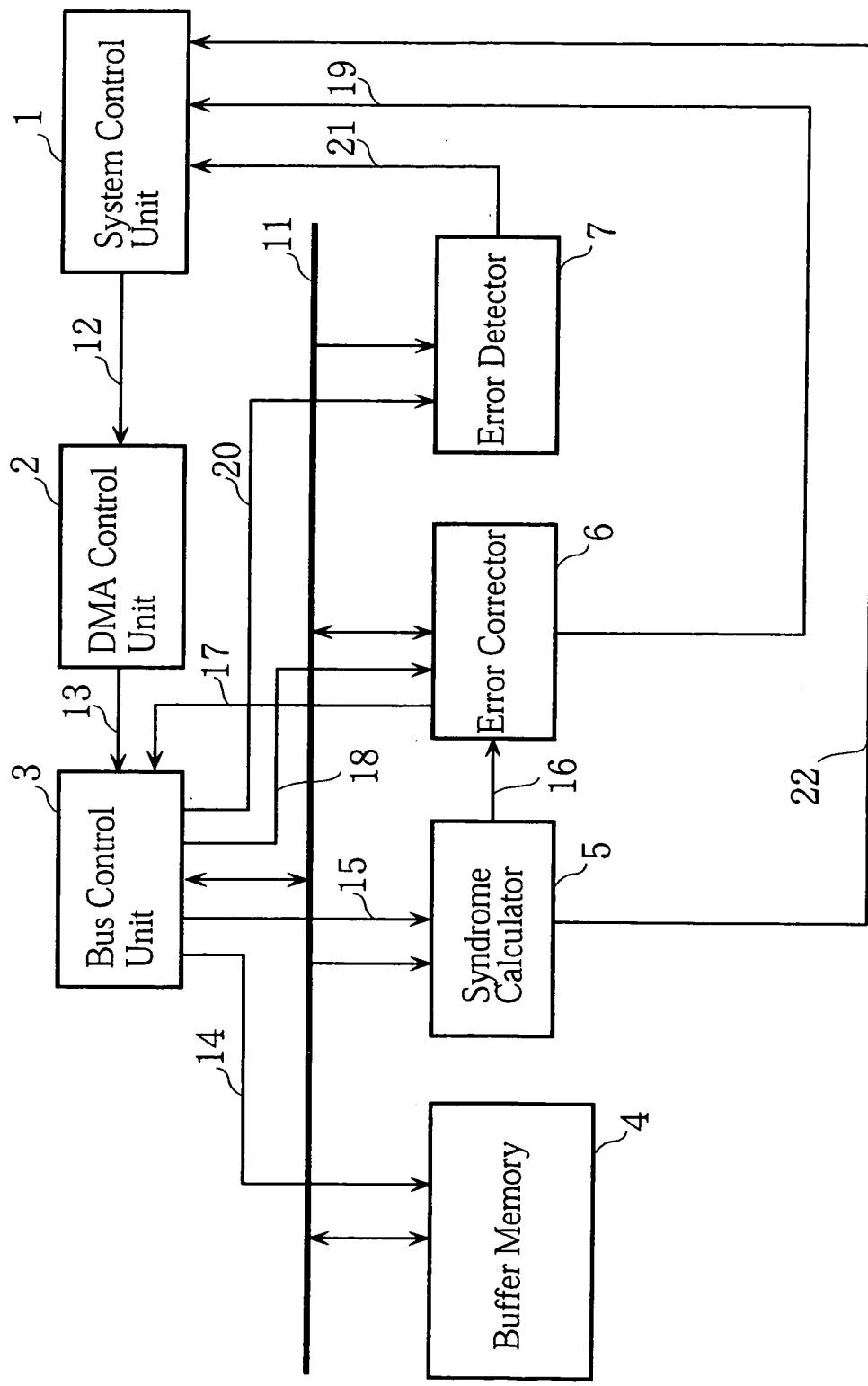


Fig. 8

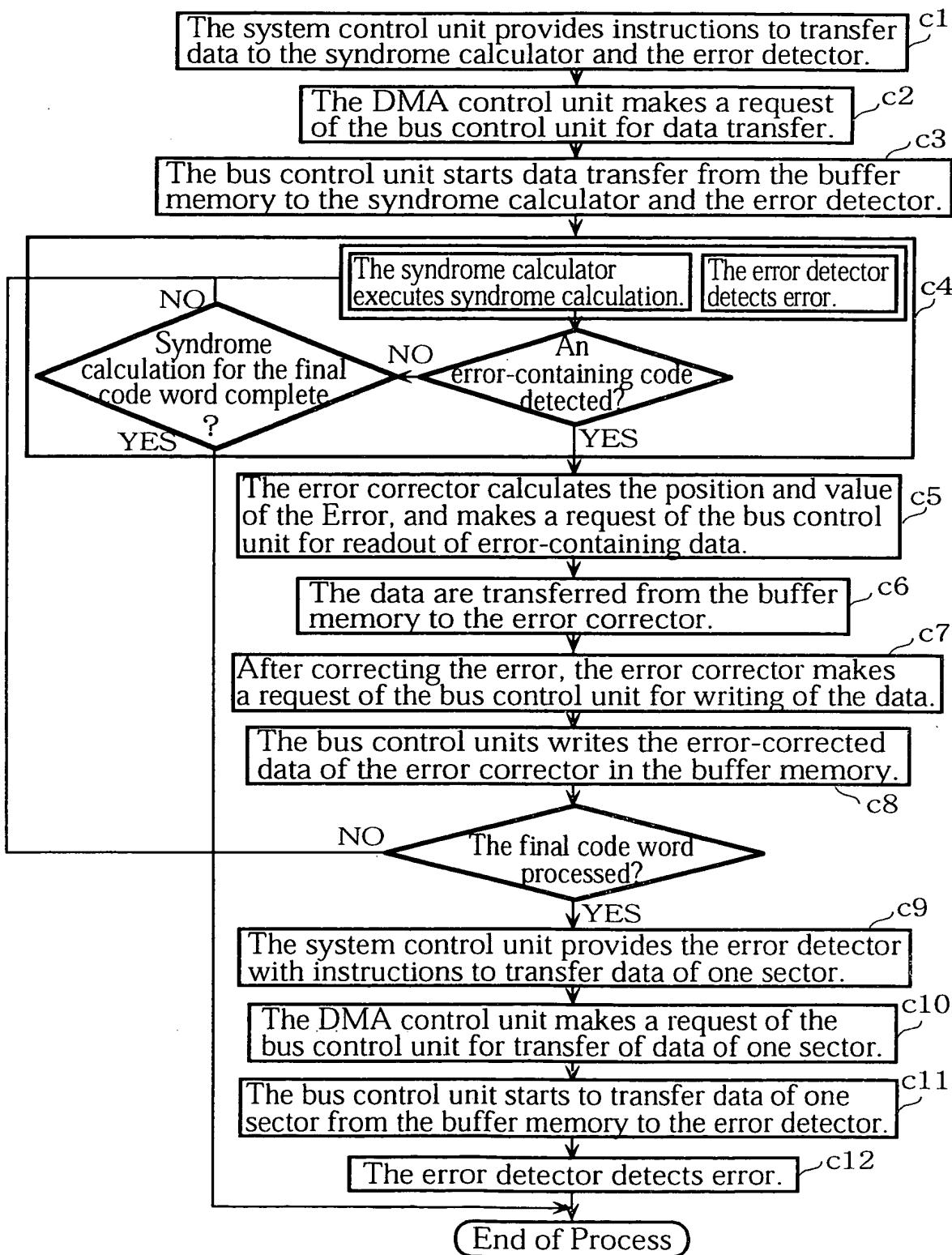


Fig. 9

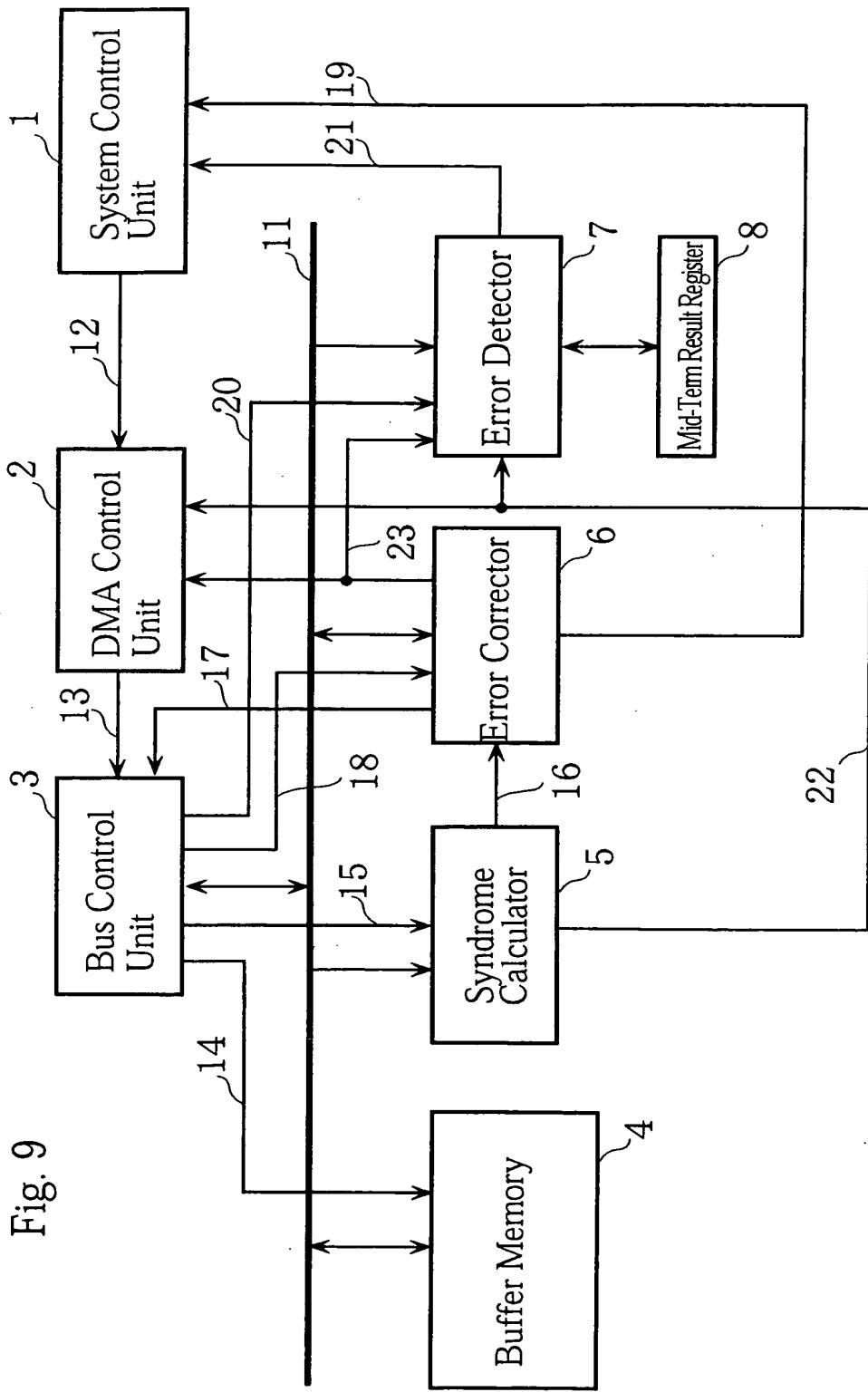


Fig. 10

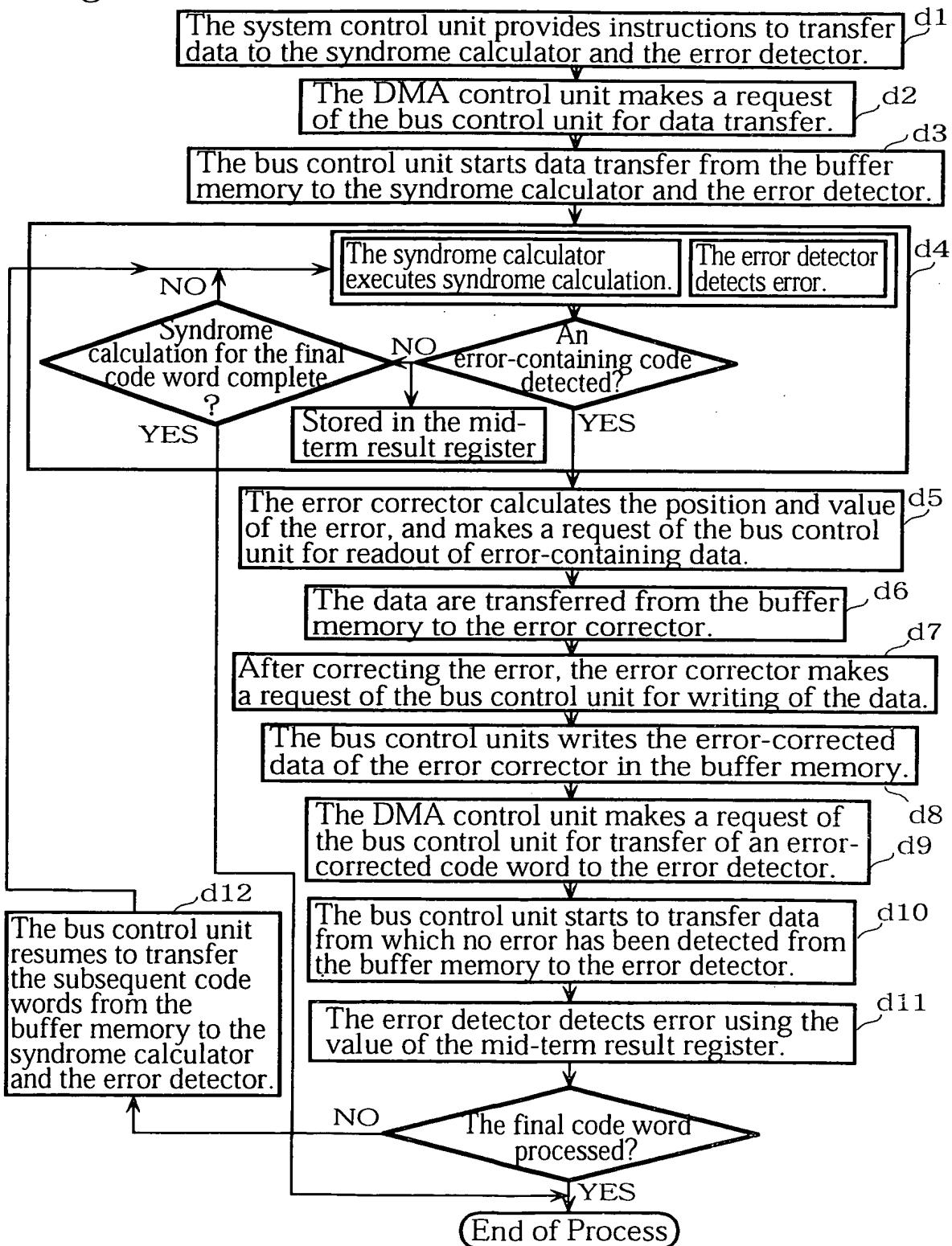


Fig. 11

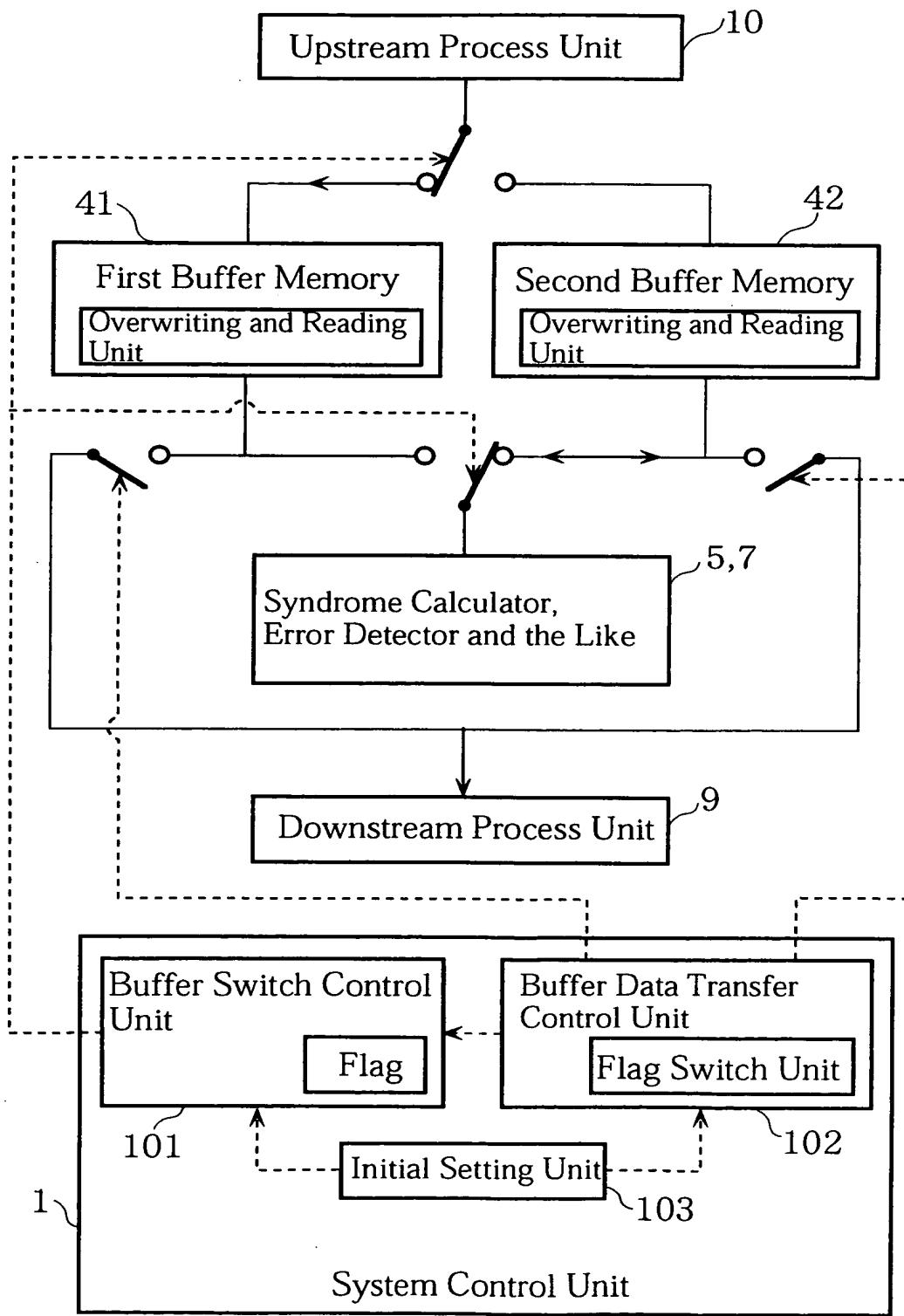


Fig. 12

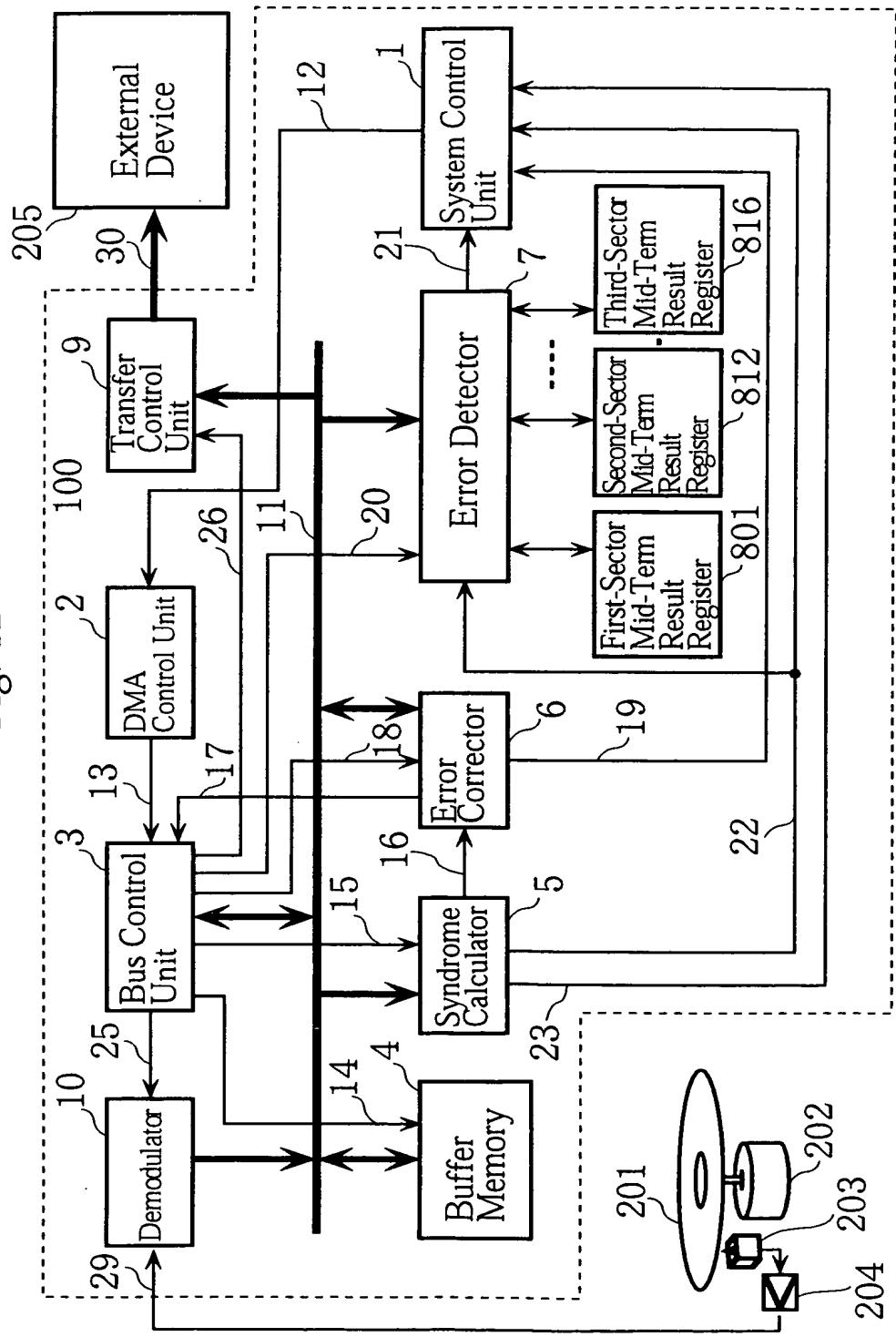


Fig. 13

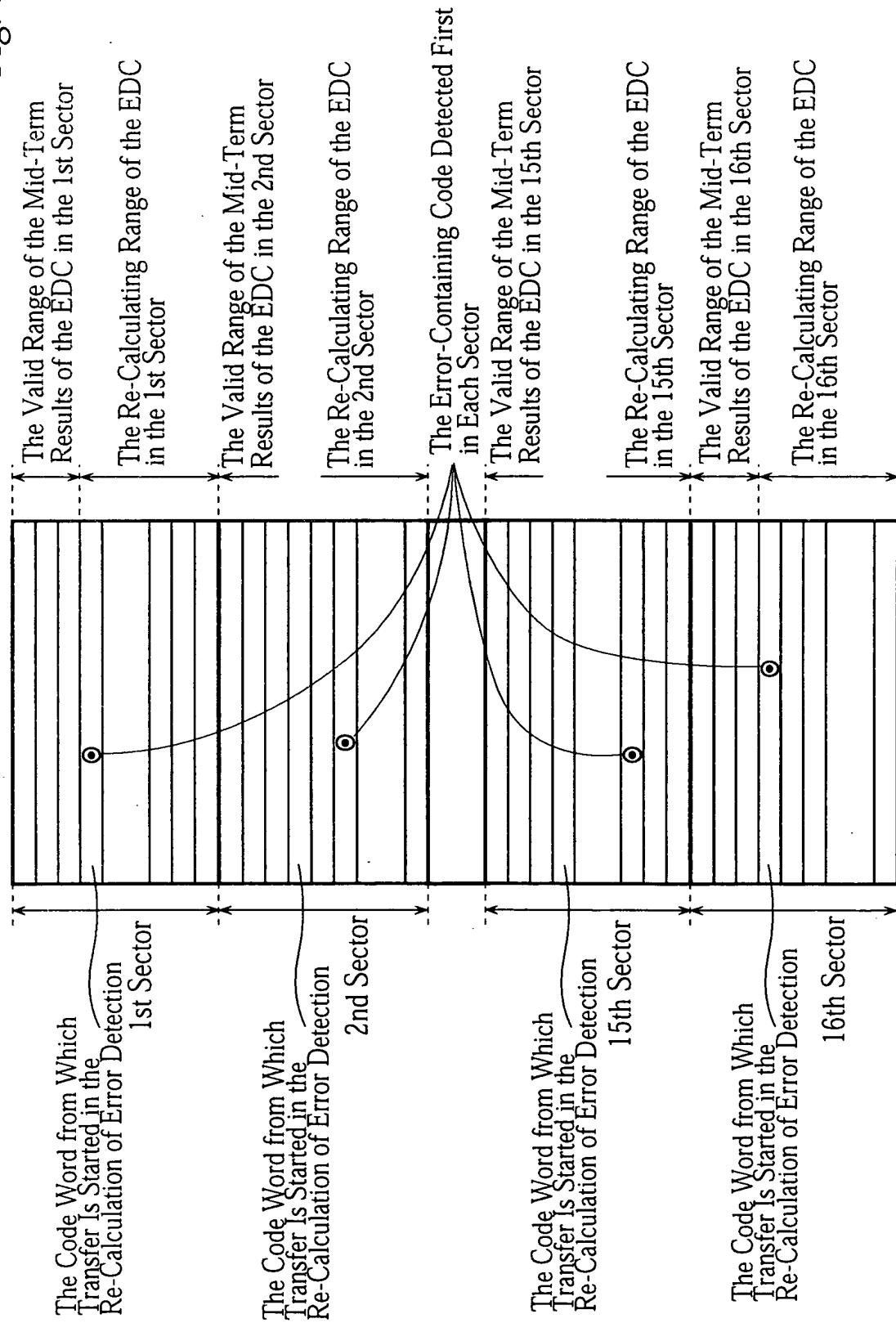


Fig. 14

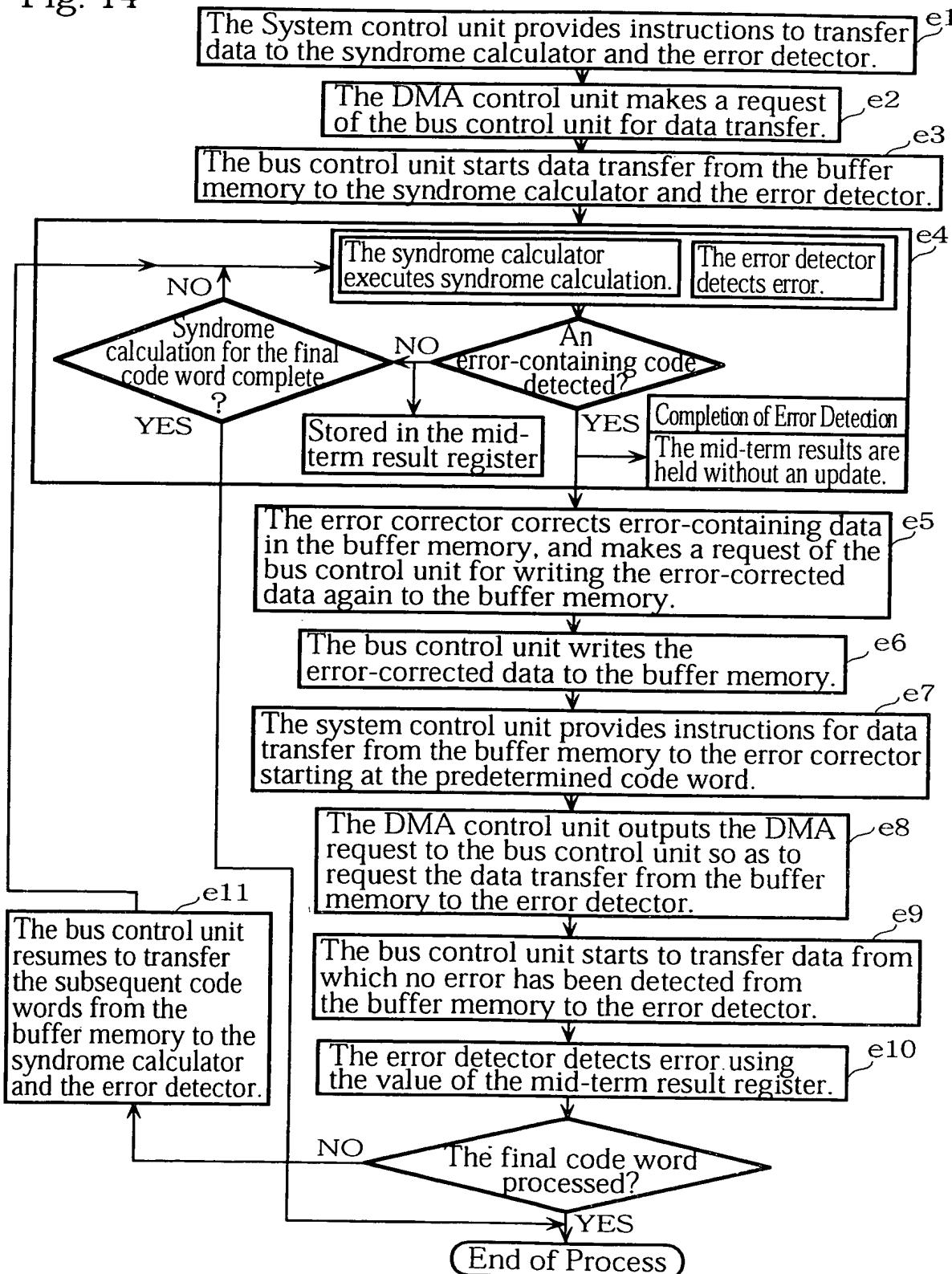


Fig. 15

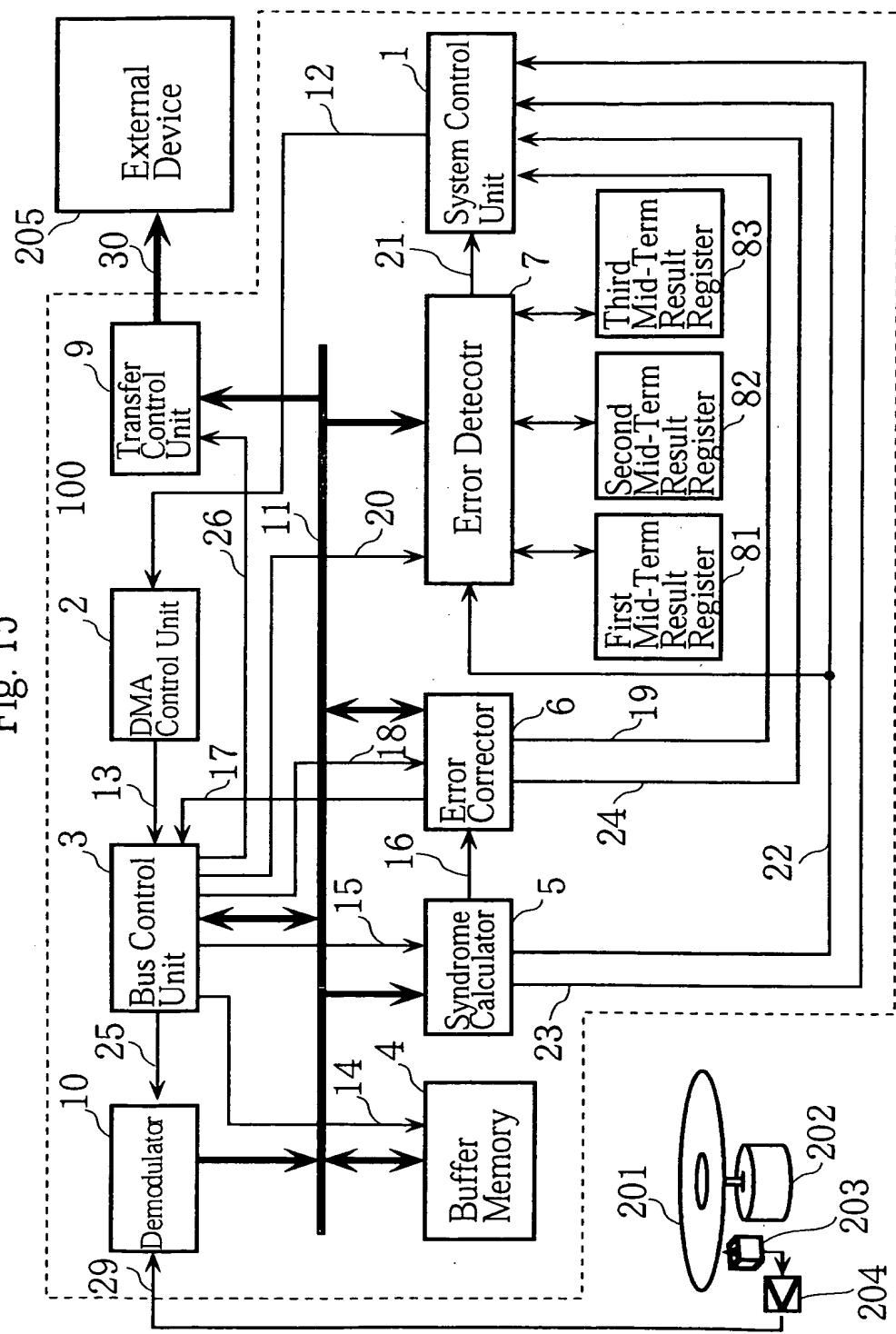


Fig. 16

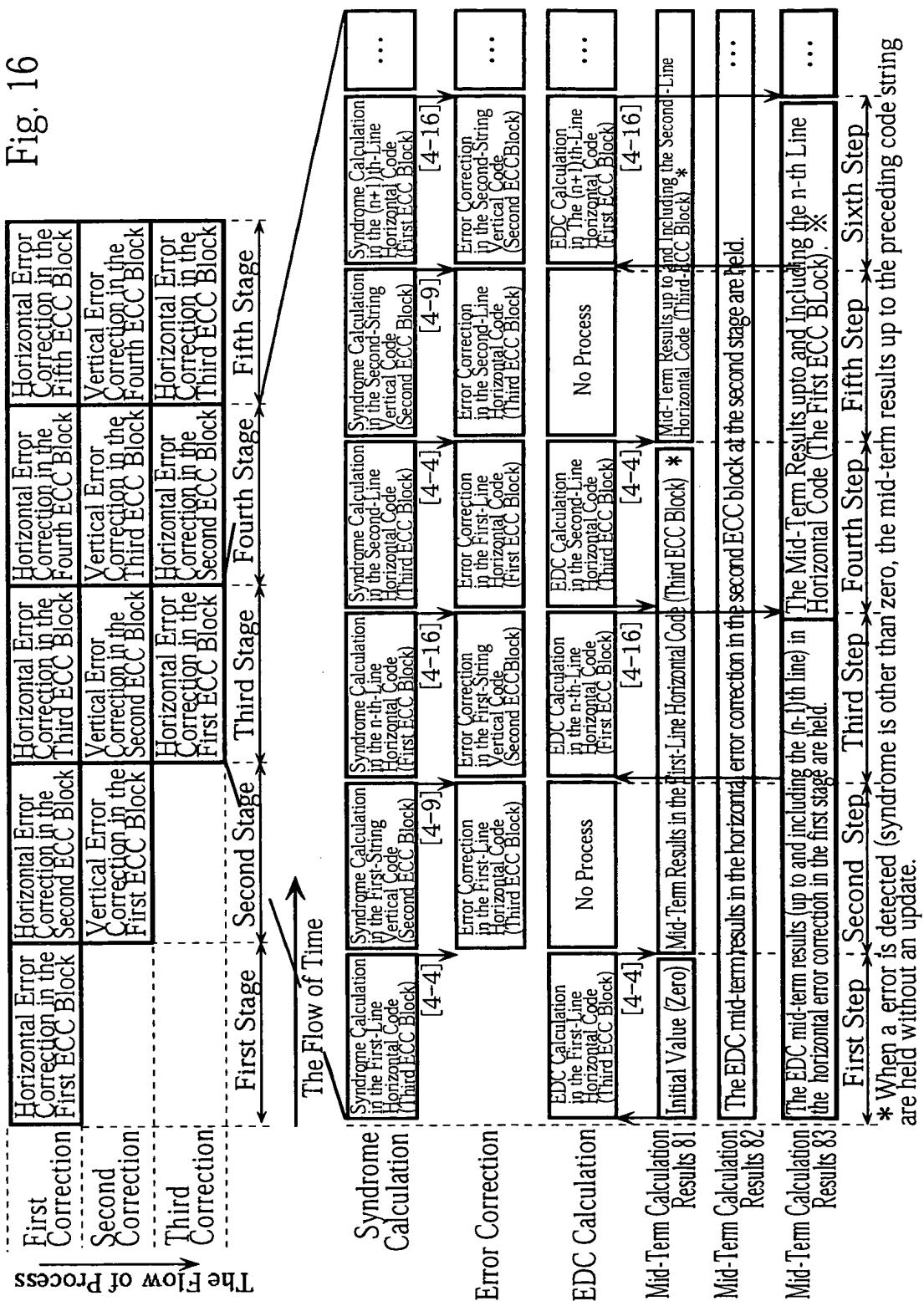


Fig. 17

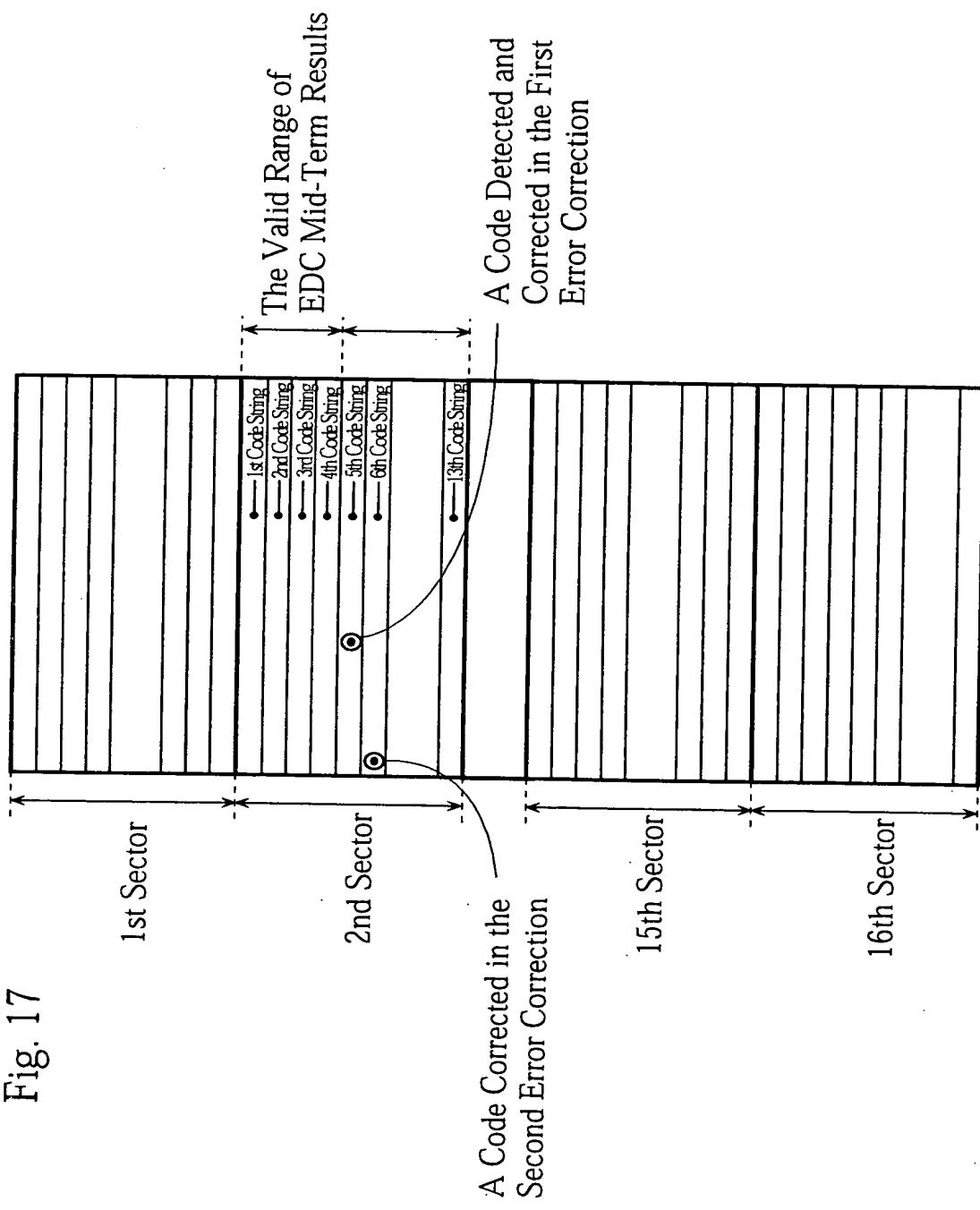


Fig. 18

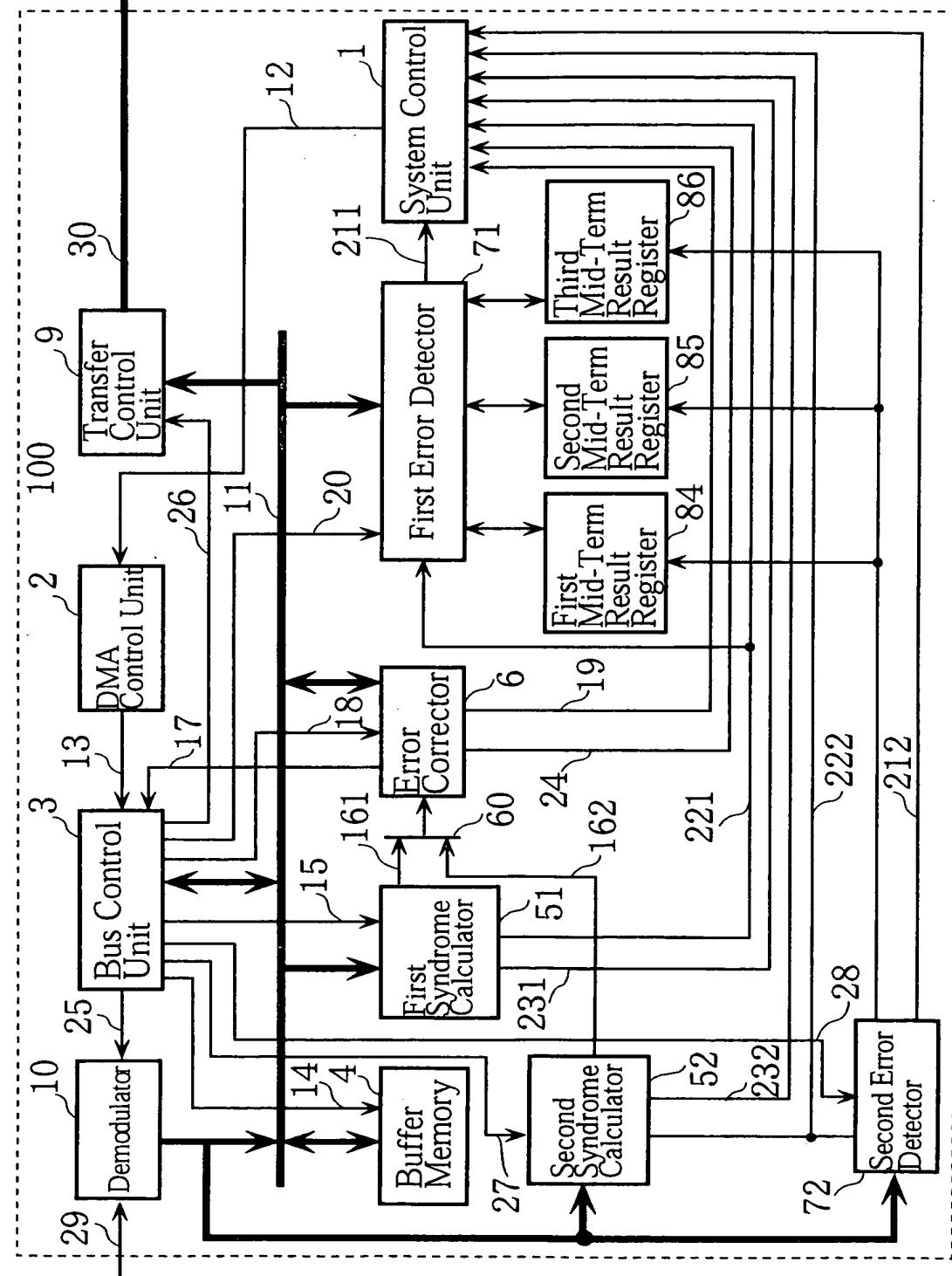


Fig. 19

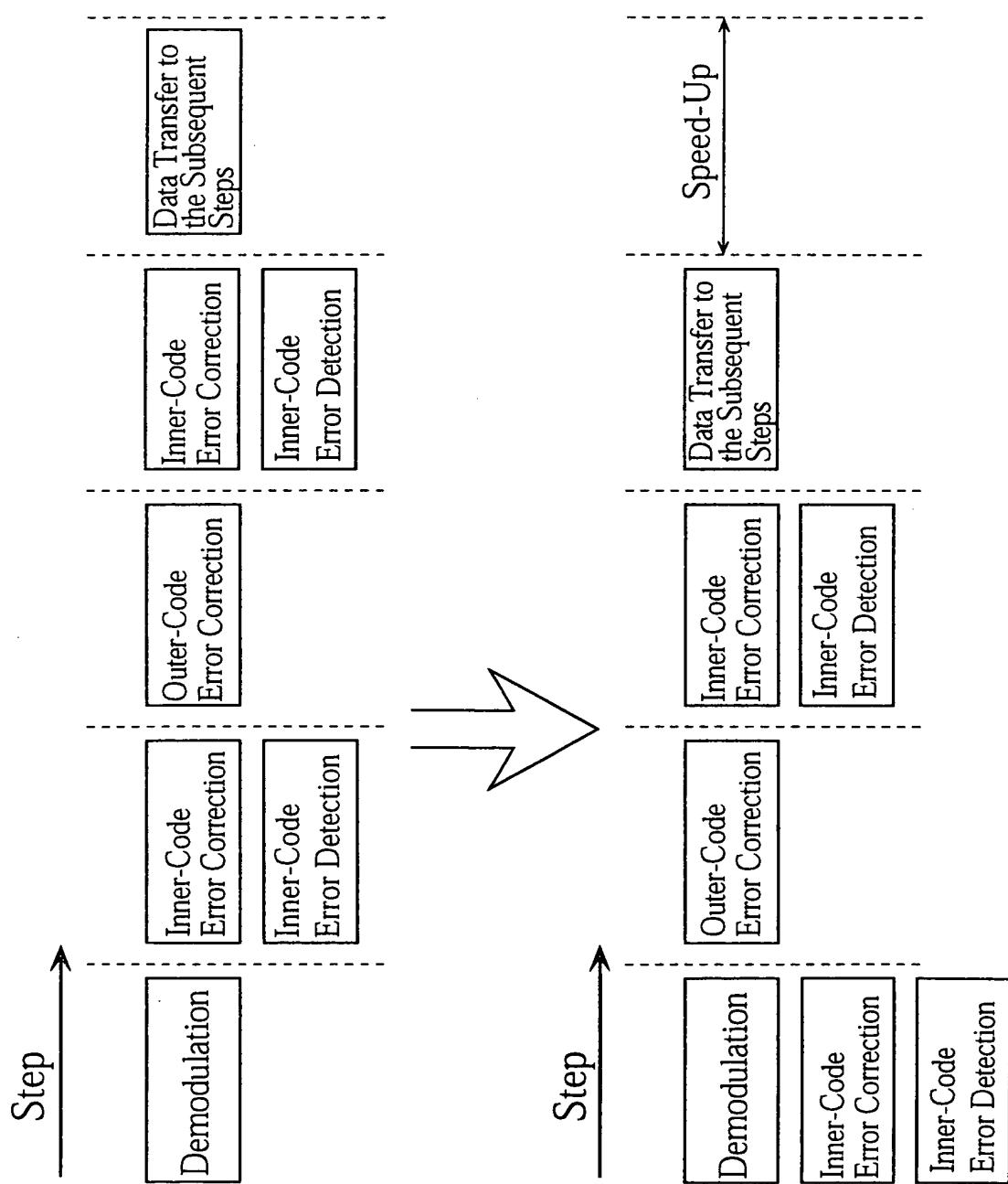


Fig. 20

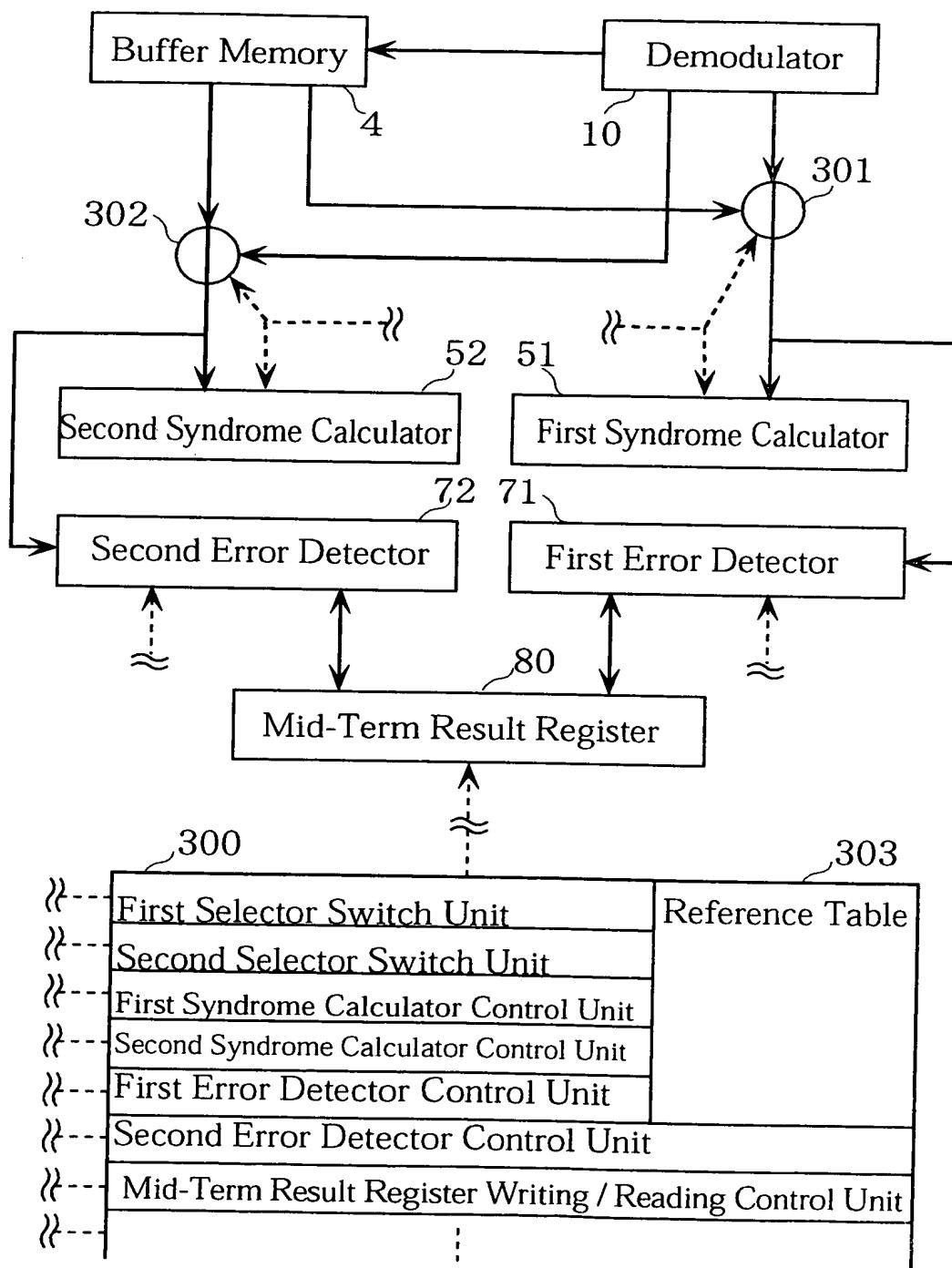


Fig. 21A

Process Unit	Processing Targets			
	ECC	Selector	Word	Vertical
First Syndrome Calculator	1	2	3	—
"		"	"	"
First Error Detector	—	—		13
"	"	"	"	
"	"	"	"	"

Fig. 21B

ECC	Site	Selector	Word	Vertical	Number of Times
First ECC	In Process of Demodulation	6	2	3	1
Second ECC	Buffer Memory	9	2	3	2
"	Buffer Memory	"	"	"	"